

Docket No.: 67162-017

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Masayuki IKETANI :
Serial No.: Group Art Unit:
Filed: July 16, 2003 Examiner:
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE CAPABLE OF SHORTENING PERIOD REQUIRED FOR PERFORMING DATA RETENTION TEST

INFORMATION DISCLOSURE STATEMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

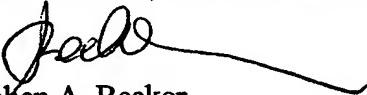
The relevance of JP 5-101649 is discussed in the present specification.

Serial No.:

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


Stephen A. Becker
Registration No. 26,527

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Date: July 16, 2003

INFORMATION DISCLOSURE
CITATION IN AN ^{MPEP}
APPLICATION

(PTO-1449)

ATTY. DOCKET NO.
67162-017

SERIAL NO.

APPLICANT
Masayuki IKETANIFILING DATE
July 16, 2003

GROUP

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code(s if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	US				

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Code-Number & Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
		JP 5-101649	04/23/1993	KUBONO		abstract	
		JP 8-106787	04/23/1996	EGAWA		abstract	

OTHER ART (Including Author, Title, Date, Permanent Pages, ETC.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

OIR 5
MAR 30 2004
TRADEMARK OFFICE

Docket No.: 67162-017

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Masayuki IKETANI : Confirmation Number: 7350
Serial No.: 10/619,476 : Group Art Unit: 2818
Filed: July 16, 2003 : Allowed: February 10, 2004
: Examiner: T. Phan

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE CAPABLE OF SHORTENING PERIOD REQUIRED FOR PERFORMING DATA RETENTION TEST

REQUEST FOR ACKNOWLEDGEMENT OF THE CITED ART

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

We are in receipt of the Notice of Allowance dated February 10, 2004.

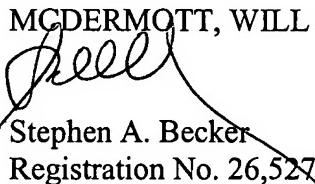
In reviewing the file, it is noted that we have not received acknowledgement of the Information Disclosure Statement and cited art filed July 16, 2003. Enclosed are copies of the Information Disclosure Statement, PTO-1449 and stamped return postcard acknowledgement.

Applicant hereby respectfully requests that:

the appropriately acknowledged PTO-1449 be furnished to Applicant; and
the record be clarified to confirm that the cited art has been considered and made of record.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


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Date: March 30, 2004



Applicant: **Masayuki IKETANI**

Title: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE CAPABLE OF SHORTENING PERIOD REQUIRED FOR PERFORMING DATA RETENTION TEST**

Date Sent: **7/16/2003** Hand Carried

Transmittal Letter

New Patent App Utility Design Cont.

Fax Electronic CIP

Div. PCT RCE Prov

Letter submitting _____ pages of drawings

Req. for Approval of Drawing Amendments

Req. for Oral Hearing

Nat. of Appeal Appeal Brief

Rule 312 Amendment/Letter Reply Brief

Req. for Acknowledgement of Cited Art

Issue Fee Publication Fee

Req. for Certificate of Correction

Maintenance Fee for _____ years after grant

Fee Address Indication Form

Terminal Disclaimer

Petition to Commissioner

Status Inquiry

Other Associate Power of Attorney

Preliminary Amendment

Response to Missing Parts Notice

Resp. to Notice to Correct App. Papers

Certified Copy of Priority Doc.

Claim for Convention Priority

Response/Amendment to Office Action of

Request for month Extension of Time

Check for \$ Charge Deposit Acct. 500417\$ 790.00

Atty Init. SAB

Trgr. # 4233

Secy or PL K/Moshinski

Early Serial Number

CMS Descrip.: 1-75000	4-40.00	THE PATENT AND TRADEMARK OFFICE DATE STAMPED HEREON IS ACKNOWLEDGEMENT THAT THE ITEMS CHECKED ABOVE, WERE RECEIVED BY THE PTO ON THE DATE STAMPED.			
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